# OsmocomBB - Bug #3554

# trxcon/scheduler: misaligned burst reception

09/13/2018 08:06 PM - fixeria

Status: New Start date: 09/14/2018

Priority: Normal Due date:

Assignee: fixeria % Done: 0%

Category: trxcon

Target version: Improvement of the higher layers of

OsmocomBB

Resolution: Spec Reference:

### Description

In some cases, as soon as a logical channel is activated, the following errors can be seen:

<0005> sched\_trx.c:263 (Re)configure TDMA timeslot #2 as TCH/H+SACCH
<0005> sched\_trx.c:420 Activating lchan=TCH/H(0) on ts=2
<0005> sched\_trx.c:420 Activating lchan=SACCH/TH(0) on ts=2
<0006> sched\_lchan\_xcch.c:96 Received incomplete data frame at fn=0 (0/104) for SACCH/TH(0)

<0006> sched\_lchan\_xcch.c:106 Received bad data frame at fn=0 (0/104) for SACCH/TH(0)

this can happen if the burst reception would start from bid != 0.

### History

#### #1 - 09/14/2018 02:08 PM - fixeria

- Subject changed from trxcon/scheduler: misaligned xCCH reception to trxcon/scheduler: misaligned burst reception

Actually, the TCH/F implementation is also affected.

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