

OsmoBSC - Bug #3833

Fix storage location of AMR S15-S0 bits

03/11/2019 05:44 PM - dexter

Status: Feedback	Start date: 03/11/2019
Priority: Normal	Due date:
Assignee: dexter	% Done: 0%
Category:	
Target version:	
Spec Reference:	
Description The current storage location of s15_s0 (lchan->activate.info.s15_s0) is incorrect by the current memory model. This should be fixed by finding a proper storage location that fits into the current memory model. See also: https://gerrit.osmocom.org/#/c/osmo-bsc/+13200/ https://gerrit.osmocom.org/#/c/osmo-bsc/+13039/	

History

#1 - 05/08/2019 05:41 PM - laforge

- Status changed from New to Feedback

both patches are merged. Does that fix/resolved this issue?

#2 - 05/28/2019 11:48 AM - dexter

Its a "cosmetic" problem. Neels has some model in mind where temporary and permanent values should be stored. I am not sure that I understand every aspect of this right. I think we should wait until neels is back and then we can fix this together. There is no pressure on this, technically its just moving a struct member to the correct sub struct to have everything consistent.