

SIMtrace 2 - Feature #1457

Feature # 1706 (Stalled): Hardware / Circuit board update for SAM3S based SIMtrace2

Spacing between JTAG and JP1/JP2

02/19/2016 10:48 PM - zecke2

Status: New	Start date:
Priority: Low	Due date:
Assignee: mschramm	% Done: 0%
Category: hardware	
Target version: PCB v2	
Spec Reference:	
Description On v1.0 and v1.1 the spacing between JTAG and JP1/JP2 (erase, test) could be higher. Right now the JTAGkey is a bit blocked by JP1/JP2.	

History

#1 - 02/20/2016 06:37 PM - laforge

- Project changed from OsmocomBB to SIMtrace
- Category deleted (244)

#2 - 05/09/2016 07:48 PM - laforge

- Priority changed from High to Low

#3 - 06/05/2018 04:57 PM - tsaitgaist

- Parent task set to #1706

#4 - 06/05/2018 04:58 PM - tsaitgaist

- Tracker changed from Bug to Feature

#5 - 08/09/2018 12:13 PM - laforge

- Project changed from SIMtrace to SIMtrace 2

#6 - 08/09/2018 12:14 PM - laforge

- Category set to hardware

#7 - 08/27/2018 07:05 AM - laforge

- Target version set to PCB v2

#8 - 09/13/2018 07:07 PM - mschramm

- Assignee changed from tsaitgaist to mschramm