

OsmoTRX - Bug #3346

osmo-trx-lms: Multi channel support: "R_CTL_LPF range limit reached"

06/13/2018 08:11 PM - laforge

Status:	New	Start date:	06/13/2018
Priority:	Normal	Due date:	
Assignee:	pespin	% Done:	0%
Category:	LimeSDR		
Target version:			
Spec Reference:			

Description

When trying to use two channels on a LimeSDR, I'm getting the following:

```
Wed Jun 13 22:07:07 2018 DMAIN <0000> LMSDevice.cpp:188 [tid=139802759618432] Setting LPFBW chan 0
Wed Jun 13 22:07:07 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 10
ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0
ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU Ref. clock: 30.72
MHz
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0
ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 16
6 ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] RX LPF configured
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] INT 75, FRAC 0, DIV_OU
TCH_CGEN 18
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] VCO 2334.72 MHz, RefCl
k 30.72 MHz
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] csw 154; interval [151
, 157]
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0
ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0
ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU Ref. clock: 30.72
MHz
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0
ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 71
ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] Filter calibrated. Fil
ter order-4th, filter bandwidth set to 5.2 MHz.Real pole 1st order filter set to 2.5 MHz. Preempha
sis filter not active
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] INT 75, FRAC 0, DIV_OU
TCH_CGEN 18
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] VCO 2334.72 MHz, RefCl
k 30.72 MHz
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] csw 154; interval [151
, 157]
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] TX LPF configured
Wed Jun 13 22:07:08 2018 DMAIN <0000> LMSDevice.cpp:193 [tid=139802759618432] Calibrating chan 0
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0
ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0
ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0
ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 11
0 ms
```

```

Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] Rx calibration finished
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0 ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0 ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0 ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 243 ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] Tx calibration finished
Wed Jun 13 22:07:08 2018 DMAIN <0000> LMSDevice.cpp:188 [tid=139802759618432] Setting LPFBW chan 1
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0 ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0 ms
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU Ref. clock: 30.72 MHz
Wed Jun 13 22:07:08 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 0 ms
Wed Jun 13 22:07:09 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] MCU algorithm time: 279 ms
Wed Jun 13 22:07:09 2018 DLMS <0001> LMSDevice.cpp:76 [tid=139802759618432] Tune Rx Filter: MCU error 12 (Rx R_CTL_LPF range limit reached)
Wed Jun 13 22:07:09 2018 DMAIN <0000> LMSDevice.cpp:207 [tid=139802759618432] Error in LMS open, closing:
Wed Jun 13 22:07:09 2018 DMAIN <0000> osmo-trx.cpp:447 [tid=139802759618432] Failed to create radio device
Shutting down transceiver...

```

So as one can see, the first RX filter can be tuned as expected, but the second filter for the second channel then fails. I don't understand enough about the LimeSDR to debug this further. Interestingly, google returns exactly zero related hits when searching for that error message, as do the LimeSuite github and the myriadrf forum.

Config file looks like this:

```

trx
bind-ip 127.0.0.1
remote-ip 127.0.0.1
base-port 5700
egprs disable
tx-sps 4
rx-sps 4
chan 0
tx-path BAND1
rx-path LNAW
chan 1
tx-path BAND2
rx-path LNAH

```

It doesn't really matter which tx-path / rx-path we are setting in chan 1, it will fail with the same error.

Related issues:

Related to OsmoTRX - Feature #2919: Native LimeSDR support

Resolved

02/09/2018

History

#1 - 06/14/2018 07:02 AM - laforge

- Related to Feature #2919: Native LimeSDR support added

#2 - 07/03/2018 03:55 PM - laforge

- Assignee set to roh

Should be fixed by this commit:

<https://github.com/myriadrf/LimeSuite/commit/40824fd1e1aaf65c436cd372ff5dbbe2990704f1>

[roh](#) please test against a recent LimeSuite (including above commit) using the config file snippet I posted above. If it does start up, it appears to be fixed. If it doesn't start and spits the "R_CTL_LPF range limit reached" error, the bug still persists.

#3 - 11/26/2018 03:19 PM - laforge

- Assignee changed from roh to pespun

the same bug can no longer be reproduced. However, osmo-trx-lms has a hard-coded check if channel == 0 and will fail on any other channel, so there's now a hard-coded limitation in osmo-trx-lms and not a problem of LimeSuite anymore.

#4 - 12/04/2018 02:02 PM - pespun

I submitted several patches to Gerrit that remove several wrong conditions and improve the logging when running with several chans.

However, I still get this LMS error message during TRX setup (but it doesn't seem it returns an error code):

```
Tue Dec 4 14:24:19 2018 DDEV <0001> LMSDevice.cpp:200 [tid=140630265782784] Calibrating chan 1
Tue Dec 4 14:24:19 2018 DLMS <0002> LMSDevice.cpp:83 [tid=140630265782784] Rx calibration: MCU error 5 (Loopb
ack signal weak: not connected/insufficient gain?)
Tue Dec 4 14:24:19 2018 DLMS <0002> LMSDevice.cpp:83 [tid=140630265782784] Tx Calibration: MCU error 5 (Loopb
ack signal weak: not connected/insufficient gain?)
```

Here's the cfg file I use with my limeSDR-USB: (network configured to use 2 TRX and tested with other devices):

```
log stderr
  logging filter all 1
  logging color 1
  logging print category 1
  logging timestamp 1
  logging print file basename
  logging level set-all info
!
line vty
  no login
!
trx
  bind-ip 192.168.30.100
  remote-ip 192.168.30.1
  base-port 5700
  eprs disable
  clock-ref internal
  tx-sps 4
  rx-sps 4
  rt-prio 18
  chan 0
    tx-path BAND1
    rx-path LNAW
  chan 1
    tx-path BAND1
    rx-path LNAW
```

Immediately after starting osmo-bts-trx, osmo-trx starts failing. It seems it's already reading samples before it is fully configured through the CTRL iface:

```
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:222 [tid=140630226585344] starting LMS...
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:330 [tid=140630226585344] chan 0: Setting TX gain to 36.5 dB
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:345 [tid=140630226585344] chan 0: Setting RX gain to 36.5 dB
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:330 [tid=140630226585344] chan 1: Setting TX gain to 36.5 dB
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:345 [tid=140630226585344] chan 1: Setting RX gain to 36.5 dB
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:392 [tid=140630226585344] Initial timestamp 27500
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:198 [tid=140630226585344] Radio started
Tue Dec 4 14:24:21 2018 DMAIN <0000> Threads.cpp:116 [tid=140630223623936] Thread 140630223623936 (task 26965)
) set name: TxLower
Tue Dec 4 14:24:21 2018 DMAIN <0000> Threads.cpp:116 [tid=140630223529728] Thread 140630223529728 (task 26966)
) set name: RxLower
Tue Dec 4 14:24:21 2018 DMAIN <0000> Threads.cpp:116 [tid=140630223369984] Thread 140630223369984 (task 26967)
) set name: RxUpper0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Threads.cpp:116 [tid=140630222173952] Thread 140630222173952 (task 26968)
) set name: TxUpper0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Threads.cpp:116 [tid=140630222046976] Thread 140630222046976 (task 26969)
) set name: RxUpper1
Tue Dec 4 14:24:21 2018 DMAIN <0000> Threads.cpp:116 [tid=140630221920000] Thread 140630221920000 (task 26970)
) set name: TxUpper1
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226585344] chan 0: command is SETRXGAIN 1
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:345 [tid=140630226585344] chan 0: Setting RX gain to 1 dB
Tue Dec 4 14:24:21 2018 DLMS <0002> LMSDevice.cpp:83 [tid=140630162028288] L
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 1 recv buffer of len 2500 expect
6b6c got 0 (0) diff=ffffffff9494
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:330 [tid=140630223529728] Receive error 0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:907 [tid=140630223529728] radio Interface receive failed
, requesting stop.
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226585344] chan 0: command is SETPOWER 0
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:330 [tid=140630226585344] chan 0: Setting TX gain to 73 dB
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226585344] chan 0: command is SETSLOT 0 4
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226585344] chan 0: command is SETSLOT 1 1
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect
6b6c got 7530 (7530) diff=9c4
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226585344] chan 0: command is SETSLOT 2 1
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 1 recv buffer of len 2500 expect
6b6c got 9c4 (9c4) diff=ffffffff9e58
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:330 [tid=140630223529728] Receive error 0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:907 [tid=140630223529728] radio Interface receive failed
, requesting stop.
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226585344] chan 0: command is SETSLOT 3 1
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226585344] chan 0: command is SETSLOT 4 1
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226585344] chan 0: command is SETSLOT 5 1
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226585344] chan 0: command is SETSLOT 6 1
3
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect
6b6c got 7ef4 (7ef4) diff=1388
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 1 recv buffer of len 2500 expect
6b6c got 1388 (1388) diff=ffffffffffa81c
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:330 [tid=140630223529728] Receive error 0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:907 [tid=140630223529728] radio Interface receive failed
, requesting stop.
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect
6b6c got 88b8 (88b8) diff=1d4c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 1 recv buffer of len 2500 expect
6b6c got 1d4c (1d4c) diff=ffffffffffb1e0
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:330 [tid=140630223529728] Receive error 0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:907 [tid=140630223529728] radio Interface receive failed
, requesting stop.
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect
6b6c got 927c (927c) diff=2710
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 1 recv buffer of len 2500 expect
6b6c got 2710 (2710) diff=ffffffffffbba4
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:330 [tid=140630223529728] Receive error 0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:907 [tid=140630223529728] radio Interface receive failed
, requesting stop.
```

Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 6b6c got 9c40 (9c40) diff=30d4
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 1 recv buffer of len 2500 expect 6b6c got 30d4 (30d4) diff=ffffffffffffc568
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:330 [tid=140630223529728] Receive error 0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:907 [tid=140630223529728] radio Interface receive failed , requesting stop.
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 6b6c got a604 (a604) diff=3a98
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 1 recv buffer of len 2500 expect 6b6c got 3a98 (3a98) diff=ffffffffffffcf2c
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:330 [tid=140630223529728] Receive error 0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:907 [tid=140630223529728] radio Interface receive failed , requesting stop.
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226585344] chan 0: command is SETSL0T 7 1 3
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 6b6c got afc8 (afc8) diff=445c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 1 recv buffer of len 2500 expect 6b6c got 445c (445c) diff=ffffffffffffd8f0
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:330 [tid=140630223529728] Receive error 0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:907 [tid=140630223529728] radio Interface receive failed , requesting stop.
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 6b6c got b98c (b98c) diff=4e20
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 1 recv buffer of len 2500 expect 6b6c got 4e20 (4e20) diff=ffffffffffffe2b4
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:330 [tid=140630223529728] Receive error 0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:907 [tid=140630223529728] radio Interface receive failed , requesting stop.
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 6b6c got c350 (c350) diff=57e4
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 1 recv buffer of len 2500 expect 6b6c got 57e4 (57e4) diff=ffffffffffffec78
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:330 [tid=140630223529728] Receive error 0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:907 [tid=140630223529728] radio Interface receive failed , requesting stop.
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 6b6c got cd14 (cd14) diff=61a8
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 1 recv buffer of len 2500 expect 6b6c got 61a8 (61a8) diff=fffffffffffff63c
Tue Dec 4 14:24:21 2018 DMAIN <0000> radioInterface.cpp:330 [tid=140630223529728] Receive error 0
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:907 [tid=140630223529728] radio Interface receive failed , requesting stop.
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 6b6c got d6d8 (d6d8) diff=6b6c
Tue Dec 4 14:24:21 2018 DMAIN <0000> Transceiver.cpp:1038 [tid=140630223529728] ClockInterface: sending IND C LOCK 2665236
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 7530 got e09c (e09c) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 7ef4 got ea60 (ea60) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 88b8 got f424 (f424) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 927c got fde8 (fde8) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect 9c40 got 107ac (107ac) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect a604 got 11170 (11170) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect afc8 got 11b34 (11b34) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect b98c got 124f8 (124f8) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect c350 got 12ebc (12ebc) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect cd14 got 13880 (13880) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect d6d8 got 14244 (14244) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect e09c got 14c08 (14c08) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect ea60 got 155cc (155cc) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 expect

pect 25d78 got 2c8e4 (2c8e4) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 2673c got 2d2a8 (2d2a8) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 27100 got 2dc6c (2dc6c) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 27ac4 got 2e630 (2e630) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 28488 got 2eff4 (2eff4) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 28e4c got 2f9b8 (2f9b8) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 29810 got 3037c (3037c) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 2ald4 got 30d40 (30d40) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 2ab98 got 31704 (31704) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 2b55c got 320c8 (320c8) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 2bf20 got 32a8c (32a8c) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 2c8e4 got 33450 (33450) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 2d2a8 got 33e14 (33e14) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 2dc6c got 347d8 (347d8) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 2e630 got 3519c (3519c) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 2eff4 got 35b60 (35b60) diff=6b6c
Tue Dec 4 14:24:21 2018 DLMS <0002> LMSDevice.cpp:83 [tid=140630162028288] L
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 2f9b8 got 36524 (36524) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 3037c got 36ee8 (36ee8) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 30d40 got 378ac (378ac) diff=6b6c
Tue Dec 4 14:24:21 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 31704 got 38270 (38270) diff=6b6c
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is RXTUNE 1782
400
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:616 [tid=140630226458368] chan 1: Setting Rx Freq to 1.7824
e+09 Hz
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 320c8 got 38c34 (38c34) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 32a8c got 395f8 (395f8) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 33450 got 39fbc (39fbc) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 33e14 got 3a980 (3a980) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 347d8 got 3b344 (3b344) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 3519c got 3bd08 (3bd08) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 35b60 got 3c6cc (3c6cc) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 36524 got 3d090 (3d090) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 36ee8 got 3da54 (3da54) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 378ac got 3e418 (3e418) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 38270 got 3eddc (3eddc) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 38c34 got 3f7a0 (3f7a0) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 395f8 got 40164 (40164) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 39fbc got 40b28 (40b28) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 3a980 got 414ec (414ec) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex

pect 51c98 got 58804 (58804) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5265c got 591c8 (591c8) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 53020 got 59b8c (59b8c) diff=6b6c
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is TXTUNE 1877
400
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:604 [tid=140630226458368] chan 1: Setting Tx Freq to 1.8774
e+09 Hz
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 539e4 got 5a550 (5a550) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 543a8 got 5af14 (5af14) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 54d6c got 5b8d8 (5b8d8) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 55730 got 5c29c (5c29c) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 560f4 got 5cc60 (5cc60) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 56ab8 got 5d624 (5d624) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5747c got 5dfe8 (5dfe8) diff=6b6c
Tue Dec 4 14:24:22 2018 DLMS <0002> LMSDevice.cpp:83 [tid=140630162028288] L
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 57e40 got 5e9ac (5e9ac) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 58804 got 5f370 (5f370) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 591c8 got 5fd34 (5fd34) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 59b8c got 606f8 (606f8) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5a550 got 610bc (610bc) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5af14 got 61a80 (61a80) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5b8d8 got 62444 (62444) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5c29c got 62e08 (62e08) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5cc60 got 637cc (637cc) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5d624 got 64190 (64190) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5dfe8 got 64b54 (64b54) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5e9ac got 65518 (65518) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5f370 got 65edc (65edc) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 5fd34 got 668a0 (668a0) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 606f8 got 67264 (67264) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 610bc got 67c28 (67c28) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 61a80 got 685ec (685ec) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 62444 got 68fb0 (68fb0) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 62e08 got 69974 (69974) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 637cc got 6a338 (6a338) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 64190 got 6acfc (6acfc) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 64b54 got 6b6c0 (6b6c0) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 65518 got 6c084 (6c084) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 65edc got 6ca48 (6ca48) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 668a0 got 6d40c (6d40c) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex

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pect 67264 got 6ddd0 (6ddd0) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 67c28 got 6e794 (6e794) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 685ec got 6f158 (6f158) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 68fb0 got 6fb1c (6fb1c) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 69974 got 704e0 (704e0) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 6a338 got 70ea4 (70ea4) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 6acfc got 71868 (71868) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 6b6c0 got 7222c (7222c) diff=6b6c
Tue Dec 4 14:24:22 2018 DLMS <0002> LMSDevice.cpp:83 [tid=140630162028288] L
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 6c084 got 72bf0 (72bf0) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 6ca48 got 735b4 (735b4) diff=6b6c
Tue Dec 4 14:24:22 2018 DLMS <0002> LMSDevice.cpp:83 [tid=140630226458368] SetNCOFrequency(index = 0) - Frequ
ency(0.4 MHz) out of range [0-0.270833) MHz
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 6d40c got 73f78 (73f78) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 6ddd0 got 7493c (7493c) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 6e794 got 75300 (75300) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 6f158 got 75cc4 (75cc4) diff=6b6c
Tue Dec 4 14:24:22 2018 DLMS <0002> LMSDevice.cpp:83 [tid=140630226458368] SetNCOFrequency(index = 0) - Frequ
ency(0.4 MHz) out of range [0-0.270833) MHz
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 6fb1c got 76688 (76688) diff=6b6c
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 704e0 got 7704c (7704c) diff=6b6c
Tue Dec 4 14:24:22 2018 DMAIN <0000> osmo-trx.cpp:449 [tid=140630265782784] Shutting down transceiver...
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:307 [tid=140630265782784] Stopping the transceiver
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:530 [tid=140630223529728] chan 0 recv buffer of len 2500 ex
pect 70ea4 got 77a10 (77a10) diff=6b6c
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:320 [tid=140630265782784] Stopping the device
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is SETTSC 7
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:826 [tid=140630226458368] Changing TSC from 7 to 7
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is SETRXGAIN 1
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:345 [tid=140630226458368] chan 1: Setting RX gain to 1 dB
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is SETPOWER 0
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:330 [tid=140630226458368] chan 1: Setting TX gain to 73 dB
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is SETSLOT 0 1
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is SETSLOT 1 7
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is SETSLOT 2 1
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is SETSLOT 3 1
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is SETSLOT 4 1
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is SETSLOT 5 1
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is SETSLOT 6 1
3
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:724 [tid=140630226458368] chan 1: command is SETSLOT 7 1
3
Tue Dec 4 14:24:22 2018 DLMS <0002> LMSDevice.cpp:83 [tid=140630153635584] popping from TX, samples popped 23
0/510
Tue Dec 4 14:24:22 2018 DMAIN <0000> Transceiver.cpp:333 [tid=140630265782784] Transceiver stopped
Tue Dec 4 14:24:22 2018 DDEV <0001> LMSDevice.cpp:62 [tid=140630265782784] Closing LMS device

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#5 - 12/04/2018 07:12 PM - pespín

#6 - 12/05/2018 04:22 PM - pespín

- Subject changed from osmo-trx-lms "R_CTL_LPF range limit reached" to osmo-trx-lms: Multi channel support: "R_CTL_LPF range limit reached"