

osmo-clock-gen - Feature #3858

Make more GPIOs available for future use

03/24/2019 03:47 PM - horiz0n

Status: Resolved	Start date: 03/25/2019
Priority: Normal	Due date: 03/25/2019
Assignee: mschramm	% Done: 100%
Category:	
Target version: hw-v2	
Spec Reference:	
Description Could you please wire some free pins and append/align them to the end of the olimex expansion connector so they could be used as additional spi chip selects?	
Related issues: Follows osmo-clock-gen - Bug #3857: Use SAMD XOSC / PLL / GCLK to allow lower... Resolved 03/24/2019	

History

#1 - 03/24/2019 08:22 PM - laforge

- Assignee set to laforge
- Target version set to hw-v2

#2 - 05/08/2019 03:12 PM - laforge

- Assignee changed from laforge to mschramm

#3 - 05/15/2019 06:51 PM - mschramm

- Due date set to 03/25/2019
- Start date changed from 03/24/2019 to 03/25/2019
- Follows Bug #3857: Use SAMD XOSC / PLL / GCLK to allow lower reference frequencies added

#4 - 05/29/2019 03:02 PM - mschramm

- Status changed from New to In Progress

#5 - 05/29/2019 05:49 PM - mschramm

- Status changed from In Progress to Resolved
- % Done changed from 0 to 100

allocated PA11, PA14 and PA15 as additional GPIOs on a pinheader (together with VDD and GND).