

E1/T1 Hardware Interface (including icE1usb) - Feature #4674

make use of USB ISO transfer header

07/21/2020 02:26 PM - laforge

Status: Resolved	Start date: 07/21/2020
Priority: Normal	Due date:
Assignee: laforge	% Done: 100%
Category: firmware	
Target version:	
Spec Reference:	
Description The RX + TX transfers have headers which are intended to be used for a variety of bits like <ul style="list-style-type: none">• CRC error reporting• LOS (loss of signal)• FER (far end error)	
Related issues: Related to E1/T1 Hardware Interface (including icE1usb) - Bug #4750: populate... Resolved 09/11/2020	

Associated revisions

Revision 69a28aaf - 12/17/2020 08:13 PM - laforge

Add support for icE1usb interrupt endpoint error reporting

Related: OS#4674

Change-Id: I5b0bf3cf40c623c17f2e88292e880b545c36b7b6

Depends: osmo-e1-hardware If157fde9d4ca05910b09537e19f37603c6d925f0

Revision a74aaf17 - 12/20/2020 10:29 AM - laforge

Add support for icE1usb interrupt endpoint error reporting

Related: OS#4674

Change-Id: I5b0bf3cf40c623c17f2e88292e880b545c36b7b6

Depends: osmo-e1-hardware If157fde9d4ca05910b09537e19f37603c6d925f0

History

#1 - 09/11/2020 01:23 PM - laforge

- Related to Bug #4750: populate new osmo-e1-hardware.git with gateway + firmware added

#2 - 12/16/2020 08:40 PM - laforge

- Status changed from New to In Progress

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20:51 < tnt> LaF0rge: yeah, an interrupt endpoint for async status like LOS/ALARM/... might make more sense and just keep the header for CRC errors.
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#3 - 12/17/2020 06:20 PM - laforge

- % Done changed from 0 to 70

- error reporting now implemented via interrupt EP: <https://gerrit.osmocom.org/c/osmo-e1-hardware/+21777>
- Rx transfer header now only used for E1 frame number: <https://gerrit.osmocom.org/c/osmo-e1-hardware/+21717>

#4 - 02/06/2021 09:49 AM - laforge

- Status changed from In Progress to Resolved

- % Done changed from 70 to 100

patches merged